

## TPoX (Transaction Processing over XML) Benchmark Result

<http://tpox.sourceforge.net/>

### Overview:

<b>Sponsor:</b>	Intel® Corporation		
<b>Date:</b>	Oct. 2009		
<b>Database System:</b>	DB2 9.7		
<b>Operating System:</b>	SUSE Linux* Enterprise server 10 SP2		
<b>Hardware Summary:</b>	Intel® Xeon® Processor X7460 server with 64GB RAM		
<b>TPoX Version:</b>	2.0	<b>TPoX Scale Factor:</b>	M - 1TB

### Primary Result:

<b>TTPS (TPoX Transactions Per Second):</b>	6654
<b>Number of concurrent users:</b>	220

### Secondary Results (optional):

<b>TIPS (TPoX Inserts Per Second):</b>	Custacc:		Users
	Orders:		Users
<b>TQPS (TPoX Queries Per Second):</b>			Users
<b>Initial database size (incl. indexes, etc.):</b>	537 GB (using DB2 data compression)		

### System under test (database server):

<b>Processors:</b> Intel® Xeon® Processor X7460, 4 processors/24 cores/24 threads, 2.67 GHz, 32KB DL1 cache per core, 32KB IL1 cache per core, 9MB L2 cache per processor, 16 MB L3 cache per processor			
<b>Memory:</b>	64GB	<b>Client/server network:</b>	1 Gb/s ethernet
<b>Storage system:</b>	EMC CX3-80	<b>Total no. of disks:</b>	135
<b>#disks for database:</b>	120 (RAID 0)	<b>#disks for log</b>	15 (RAID 0)
<b>Adapters:</b>	1 QLE2464 Qlogic Fiber Channel 4 Gb/s adapter		
<b>Other details:</b>	All disks are SCSI, 15K RPM		

### Client machine (if applicable):

<b>Machine type, OS:</b>	Intel Xeon Processor E5440 EP Server, Linux SLES 10 SP2		
<b>Number of CPUs:</b>	2	<b>Cores per CPU:</b>	4
<b>Clock Frequency:</b>	2.83 GHz	<b>Client/server network:</b>	1 Gb/s ethernet
<b>Memory:</b>	24 GB	<b>Java Level:</b>	1.6

*Other key TPoX Workload Driver parameters used to obtain the TTPS result:*

<b>-r (ramp up time):</b> 3600 seconds	<b>-ti (measurement time):</b> 3600 seconds
<b>-tt (think time):</b> 0 (default)	<b>-cc (commit count)</b> 1 (default)
<b>-tr (max transactions):</b> n/a	
<b>Other non-default parameters:</b>	

*Section for additional details, comments, graphs, or comparison to other systems.*

---

- Graphic to be inserted -

Testing and results generated by Intel engineers in Intel labs with IBM collaboration

#### Notices

Software and workloads used in performance tests may have been optimized for performance only on Intel microprocessors. Performance tests, such as SYSmark and MobileMark, are measured using specific computer systems, components, software, operations and functions. Any change to any of those factors may cause the results to vary. You should consult other information and performance tests to assist you in fully evaluating your contemplated purchases, including the performance of that product when combined with other products.

Copyright © 2011 Intel Corporation. All rights reserved

Other names and brands may be claimed as the property of others

Intel compilers, associated libraries and associated development tools may include or utilize options that optimize for instruction sets that are available in both Intel and non-Intel microprocessors (for example SIMD instruction sets), but do not optimize equally for non-Intel microprocessors. In addition, certain compiler options for Intel compilers, including some that are not specific to Intel micro-architecture, are reserved for Intel microprocessors. For a detailed description of Intel compiler options, including the instruction sets and specific microprocessors they implicate, please refer to the “Intel Compiler User and Reference Guides” under “Compiler Options.” Many library routines that are part of Intel compiler products are more highly optimized for Intel microprocessors than for other microprocessors. While the compilers and libraries in Intel compiler products offer optimizations for both Intel and Intel-compatible microprocessors, depending on the options you select, your code and other factors, you likely will get extra performance on Intel microprocessors.

Intel compilers, associated libraries and associated development tools may or may not optimize to the same degree for non-Intel microprocessors for optimizations that are not unique to Intel microprocessors. These optimizations include Intel Streaming SIMD Extensions 2 (Intel SSE2), Intel Streaming SIMD Extensions 3 (Intel SSE3), and Supplemental Streaming SIMD Extensions 3 (Intel SSSE3) instruction sets and other optimizations. Intel does not guarantee the availability, functionality, or effectiveness of any optimization on microprocessors not manufactured by Intel. Microprocessor-dependent optimizations in this product are intended for use with Intel microprocessors.

While Intel believes our compilers and libraries are excellent choices to assist in obtaining the best performance on Intel and non-Intel microprocessors, Intel recommends that you evaluate other compilers and libraries to determine which best meet your requirements. We hope to win your business by striving to offer the best performance of any compiler or library; please let us

know if you find we do not.  
Notice revision #20110228